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1 Manufacturing applications: Manufacturing 3: discrete event simulation in automotive final process system



Vishvas Patel, James Ashby, John Ma

 December 2002 **Proceedings of the 34th conference on Winter simulation: exploring new frontiers**

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The Final Process System is an important part of the entire quality assurance system in the automobile manufacturing process. Operators and machines perform a series of crucial testing procedures before shipping a vehicle. Many complex factors impact the system throughput. The important ones are first time success rate, repair and service routing logic, process layout, operator staffing, capacity of testing equipment and random equipment breakdown. Discrete Event Simulation is a tool of choic ...

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Kenneth R. Anderson, Gregory W. Diehl

 December 1988 **Proceedings of the 20th conference on Winter simulation**

Full text available: pdf(524.08 KB)

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The ability to quickly model the production implications of a product for the marketing, sales, finance, or engineering functions of the enterprise, takes the manufacturing function into a strategic planning role. Rapid modeling can play a significant role in the selection of a manufacturing strategy. This discussion will show the capability and potential of rapid modeling in assisting planners and analysts examine various manufacturing alternatives. The proposed modeling technique can be u ...

2 [Manufacturing applications: Manufacturing modeling methods: virtual reality simulation of a mechanical assembly production line](#)

Deogratias Kibira, Chuck McLean

 December 2002 **Proceedings of the 34th conference on Winter simulation: exploring new frontiers**

Full text available: pdf(416.71 KB)

 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

This paper presents our work on the application of virtual-reality simulation to the design of a production line for a mechanically-assembled product. The development of this simulation was undertaken as a part of the Manufacturing Simulation and Visualization Program at the National Institute of Standards and Technology in Gaithersburg, MD. The major research problem is the partitioning and analysis of the assembly operation of the prototype product into different tasks and allocation of the ...

3 [Authentication and authorization: Silicon physical random functions](#)

Blaise Gassend, Dwaine Clarke, Marten van Dijk, Srinivas Devadas

 November 2002 **Proceedings of the 9th ACM conference on Computer and communications security**

Full text available: pdf(433.69 KB)

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We introduce the notion of a Physical Random Function (PUF). We argue that a complex integrated circuit can be viewed as a silicon PUF and describe a technique to identify and authenticate individual integrated circuits (ICs). We describe several possible circuit realizations of different PUFs. These circuits have been implemented in commodity Field Programmable Gate Arrays (FPGAs). We present experiments which indicate that reliable

authentication of individual FPGAs can be performed even in the ...

Keywords: identification, physical random function, physical security, smartcard, tamper resistance, unclonability

4 Manufacturing applications: Simulation based design for a shipyard manufacturing process



D. J. Medeiros, Mark Traband, April Tribble, Rebekah Lepro, Kenneth Fast, Daniel Williams
December 2000 **Proceedings of the 32nd conference on Winter simulation**

Full text available:  [pdf\(994.39 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

Discrete event simulation can be used for virtual prototyping of new manufacturing facilities. Models built for this purpose must be easy to use, flexible, and provide a realistic graphical view of the proposed system. The DESTINY project has developed models of plate processing operations to assist shipyards in modernizing their plate fabrication lines. Configuration information for a proposed line is collected via a Web interface, which then launches a program to build and execute a simulation ...

5 When bad things happen to good chips (panel session)



Nagaraj NS, Andrzej Strojwas, Sani R. Nassif, Ray Hokinson, Tak Young, Wonjae L. Kang, David Overhauser, Steve Kang
June 2000 **Proceedings of the 37th conference on Design automation**

Additional Information: [full citation](#), [abstract](#), [index terms](#)

Design of reliable chips with high yield is an extremely challenging task in UDSM technologies. Time to market pressures, which often limit the necessary verification before tape-out, typically are manifested as ramp-to-production problems on "good" designs either in the manufacturing process or in the field. Burn-in process, a reactive measure to ship reliable chips, is not effective for high volume designs. Another cause for concern is hidden failures that go undetected due to ...

6 Introduction to simulation (tutorial session)



Arne Thesen, Laurel E. Travis
December 1990 **Proceedings of the 22nd conference on Winter simulation**

Full text available:  [pdf\(958.53 KB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

7 Modeling at the machine-control level using discrete event simulation (DES)



Raid Al-Aomar, Daniel Cook
December 1998 **Proceedings of the 30th conference on Winter simulation**

Full text available:  [pdf\(64.74 KB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

8 Before dynamic simulation: systematic layout design from scratch



David P. Sly
December 1997 **Proceedings of the 29th conference on Winter simulation**

Full text available:  [pdf\(386.59 KB\)](#) Additional Information: [full citation](#), [index terms](#)


9 A systematic approach to factory layout and design with FactoryPLAN, FactoryOPT, and FactoryFLOW



David P. Sly

November 1996 **Proceedings of the 28th conference on Winter simulation**Full text available:  [pdf\(334.97 KB\)](#) Additional Information: [full citation](#), [citations](#)**10 Synthesis of manufacturable analog circuits**

Tamal Mukherjee, L. R. Carley, R. A. Rutenbar

November 1994 **Proceedings of the 1994 IEEE/ACM international conference on Computer-aided design**Full text available:  [pdf\(867.35 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We describe a synthesis system that takes operating range constraints and inter- and intra-circuit parametric manufacturing variations into account while designing a sized and biased analog circuit. Previous approaches to CAD for analog circuit synthesis have concentrated on nominal analog circuit design, and subsequent optimization of these circuits for statistical fluctuations and operating point ranges. Our approach simultaneously synthesizes and optimizes for operating and manufacturing ...

11 Geometric reasoning for machining features using convex decomposition

Douglas L. Waco, Yong Se Kim

June 1993 **Proceedings on the second ACM symposium on Solid modeling and applications**Full text available:  [pdf\(1.12 MB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)**12 Book review: Intelligent Systems in Business: Integrating the Technology Edited by J. Jeffrey Richardson and Marjorie J. DeFries (Ablex, Norwood, N J, 1990)**

Norman R. Nielsen

July 1991 **ACM SIGART Bulletin**, Volume 2 Issue 4Full text available:  [pdf\(238.90 KB\)](#) Additional Information: [full citation](#), [abstract](#)

This book is a collection of 22 papers and represents the proceedings of a forum on "Increasing Management Productivity with Intelligent Systems" held in Snowmass/Aspen, Colorado, in August 1988. The proceedings are aimed at the managerial reader, and the content is intended to be relatively nontechnical. The purpose of these proceedings is "to help managers learn, not about techniques, but about applications of intelligent systems to the business problems of today" and to provide nontechnical " ...

13 Manufacturing applications: Manufacturing 3: discrete event simulation in automotive final process system

Vishvas Patel, James Ashby, John Ma

December 2002 **Proceedings of the 34th conference on Winter simulation: exploring new frontiers**Full text available:  [pdf\(165.60 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

The Final Process System is an important part of the entire quality assurance system in the automobile manufacturing process. Operators and machines perform a series of crucial testing procedures before shipping a vehicle. Many complex factors impact the system throughput. The important ones are first time success rate, repair and service routing logic, process layout, operator staffing, capacity of testing equipment and random equipment breakdown. Discrete Event Simulation is a tool of choice ...

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-  1. **Discrete event simulation in automotive final process system**
Patel, V.; Ashby, J.; Ma, J.;
Simulation Conference, 2002. Proceedings of the Winter
Volume 1, 8-11 Dec. 2002 Page(s):1030 - 1034 vol.1
Digital Object Identifier 10.1109/WSC.2002.1172997
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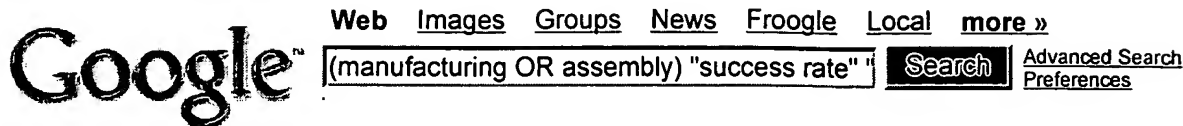
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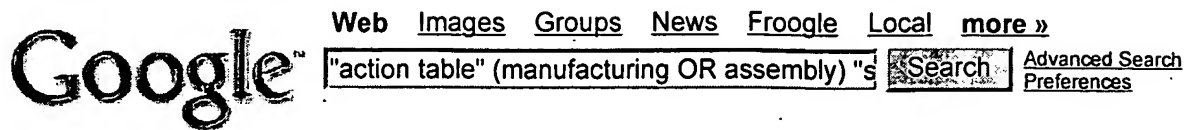


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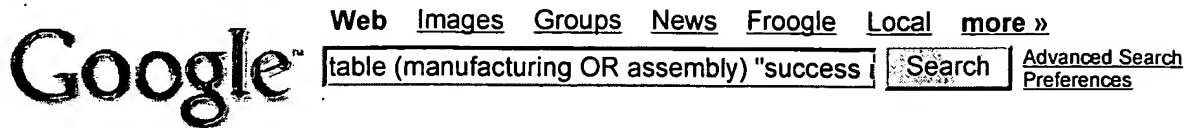
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